

App. No. 10/506,826
Office Action Dated June 29, 2005

REMARKS

Favorable reconsideration of this application is requested in view of the above amendments and the following remarks. Claims 1 and 5 are hereby amended. Claims 8-11 are canceled without prejudice or disclaimer.

Amendment of claim 1 is supported by Figures 1-4. Amendment of claim 5 is supported by Figures 13-19.

Applicants request that Perez (US 6,847,103) be listed on the PTO 892 form.

Claims 1 and 2 were rejected as being unpatentable over Perez (US 6,847,103) in view of Oida (US 6,291,274). Applicants traverse this rejection. The rejection relies on Perez to teach the semiconductor device configuration required by claim 1. However, Perez does not disclose or suggest a semiconductor device wherein a die pad, a pair of electrode terminals and a narrow patterned conductor in combination provide an overall conductor pattern which is asymmetrical with respect to a longitudinal centerline of an insulating substrate, the narrow patterned conductor extending obliquely and being offset from the longitudinal centerline of the insulating substrate, as required by claim 1. Rather, Perez teaches a semiconductor package including a frame (14) having a plurality of leads (16, the rejection equates to the claimed electrode terminals) arranged in symmetry with respect to a centerline. See Figure 2.

In contrast, the claimed offset required by claim 1 provides an overall conductor pattern of the combination of the die pad, the electrode terminals, and the narrow patterned conductor that is asymmetrical with respect to the longitudinal centerline of the substrate. The asymmetry permits accurate positioning of a semiconductor chip on the die pad.

Favorable reconsideration of claims 1 and 2 is requested.

Claims 3 and 4 were rejected as being unpatentable over Perez, in view of Oida, and further in view of Kwan (US 6,872,661). Applicants traverse this rejection. Claims 3 and 4 should be considered allowable for at least the same reasons as claim 1, from which they depend.

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Kwan does not remedy the deficiencies of Perez and Oida, as previously noted. Favorable reconsideration of claims 3 and 4 is requested.

Claims 5, 6, 8, and 11 were rejected as being unpatentable over Perez. Applicants traverse this rejection. Claims 8 and 11 are canceled. Applicants are not conceding the correctness of the rejection as applied to claims 8 and 11. Perez does not disclose or suggest a semiconductor device including a narrow patterned conductor positioned on a longitudinal centerline of an insulating substrate, nor a diagonal line of the semiconductor chip located on the longitudinal centerline of the insulating substrate, as required by claim 5. Rather, Perez teaches tie-bars (24, the rejection equates to the claimed narrow patterned conductor) positioned away from any centerline. Further, a diagonal line of the semiconductor die (56) taught by Perez is not positioned on any centerline. See Figures 2 and 7. Favorable reconsideration of claims 5 and 6 is requested.

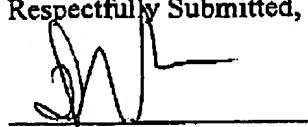
Claims 7 and 9 were rejected as being unpatentable over Perez and further in view of Oida. Applicants traverse this rejection. Claim 9 is canceled. Claim 7 should be considered allowable for at least the same reasons as claim 5, from which it depends. Applicants are not conceding the correctness of the rejection as applied to the rejected claims. Favorable reconsideration of claim 7 is requested.

Claim 10 was rejected as being unpatentable over Perez in view of Kwan. Applicants traverse this rejection. Claim 10 is canceled. Applicants are not conceding the correctness of the rejection as applied to claim 10.

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In view of the above, favorable reconsideration in the form of a notice of allowance is requested. Any questions regarding this communication can be directed to the undersigned attorney, Douglas P. Mueller, Reg. No. 30,300, at (612)455-3804.

Respectfully Submitted,


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